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4/8/03
PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of)
) Art Unit: 2812
MARTEL, Stephanie)
) Examiner: LATTIN, Christopher W
Serial No: 09/964,472)
)
Filed: September 28, 2001,)
)

Agent's Docket no. 11759

April 2, 2003

For: METHOD OF MAKING HIGH-VOLTAGE BIPOLAR/CMOS/DMOS (BCD)
DEVICES

Honorable Commissioner of Patent and Trademarks
Washington, D.C. 20231

FAX RECEIVED

APR 02 2003

RESPONSE

TECHNOLOGY CENTER 2800

Sir:

In response to the office action mailed January 2, 2003, the applicant herewith submits the following amendments and remarks.

In the Abstract

Please substitute the following abstract for the abstract on file:

-- A process for making integrated circuits of different type is described wherein sequence of mask steps is applied to a substrate or epitaxial layer of p-type material. The sequence is chosen from a predefined common set of mask steps according to the particular type of integrated circuit to be fabricated. In this way, various types of integrated circuit can be fabricated in a most efficient manner.